Atsuhiro TOBITA Serial No. 10/615,783 Response to Office Action dated October 15, 2004

Amendments to the Specification:

Please replace the paragraph beginning on page 1, line 9 with the following amended paragraph:

FIG. 2 shows a conventional synchronous rectifier circuit. SW1 indicates a main switch. SW2 indicates a rectifier switch. SW3 indicates a commutating switch. T indicates a transformer. T1 indicates a primary winding of the transformer T. T2 indicates a secondary winding of the transformer T. L indicates an output choke. C indicates a smoothing capacitor. R indicates a resistor. The switches SW1, SW2, and SW3 of this conventional example are all formed by FETs FET. One end of the resistor R is connected to the gate of the rectifier switch SW2. The other end of this resistor R is connected to both the output choke L and one end of the secondary winding T2.